

Amendments to the Specification:

Please replace the paragraph beginning at page 10, line 16, with the following amended paragraph:

The external circuit is constituted by a power source 504 which includes a stabilized power source and a high-speed and high-precision operational amplifier, a voice processing circuit 502, an external interface port 505, and a transmitting/receiving circuit 515. The CPU 506 contains a video signal processing circuit 507. To the CPU 506 are connected VRAM 511, DRAM 509, flush memory 510 and memory card 503. The data processed by the CPU ~~[[503]]~~ 506 is sent as a video signal (data signal) to the control circuit 512 from the video signal processing circuit 507. The control circuit 512 supplies a video signal and a clock to the display device 513. Concretely speaking, the control circuit has a function for distributing the video signals into data corresponding to the pixels in the display device, and a function for converting the horizontal synchronizing signals and vertical synchronizing signals input from the external unit into start signals for the drive circuit and into timing control signals for alternating the current in the embedded power-source circuit. In addition, a microphone 508 and a speaker 514 are also included in this system.